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(71) Applicant: ALNAIR LABS:KK

NATIONAL INSTITUTE OF

ADVANCED INDUSTRIAL &

TECHNOLOGY

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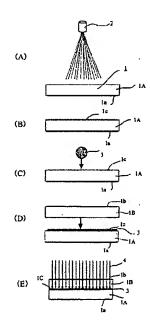
(54) SATURABLE ABSORPTION CHIP, ITS MANUFACTURING METHOD, AND DEVICE **USING SATURABLE ABSORPTION CHIP**

(57) Abstract:

PROBLEM TO BE SOLVED: To solve the problems with saturable absorption chips constituted by using carbon nanotubes that the chips are in a mechanically brittle state and have the structure inadequate for mass production and to improve saturable absorption.

SOLUTION: The invention comprises reinforcing the structure which is heretofore mechanically brittle by using two sheets of optical materials. In combination, the saturable absorption chips which are made suitable for mass production by using such optical materials and are provided with the chip structure having the stable saturable absorption characteristics to suppress reflection loss, the method for manufacturing the same and the saturable absorption device using the saturable absorption chips are included.

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JAPANESE PATENT OFFICE

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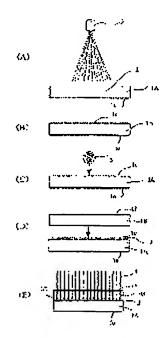
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> **[®]Title**: JP2004280028A2: SATURABLE ABSORPTION CHIP, ITS MANUFAC' METHOD, AND DEVICE USING SATURABLE ABSORPTION CHIP

Saturable absorption chip for optical communication device, has pair of Prwent Title:

substrates made of transparent optical material, between which carbon

nanotube is fixed using ultraviolet cured resin [Derwent Record]

JP Japan **P**Country:

> A2 Document Laid open to Public inspection i

SAKAKIBARA YOICHI:

> **TOKUMOTO MADOKA:** KATAURA HIROMICHI: **ACHINAMI HIROTSUGU**;

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Family: None

8 Other Abstract None

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